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PATENT NUMBER and ISSUE DATE

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U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10082294	02/26/2002	327	21: A	3943	622
**APPLICANT	S: Hoshide	a Takahir	0;		
**CONTINUIN	G DATA VERIFIED):			
* FOREIGN A	PPLICATIONS VE	RIFIED:			
JAPAN 2001-3	90151 12/21/2001				
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erified and Ackno	wledged Examiners's in	tials			027260-516
ITLE : DC bias	s control circuit, opt	tical recei	ver, and DC b	as contr	ol method U.S.DEPT. OF COMM./PAT.& TM-PTO-436L(Rev 12-9-
					12-35 Table 11 10 10 10 10 10 10 10 10 10 10 10 10

NOTICE OF ALLO	OWANCE MAILED		CLAIMS ALLOWED				
		Assistant Examiner	Total Claims		Print Claim for O.G		
ISSUE FEE			DRAWING				
Amount Due	Date Paid		Sheets Drwg.	Figs.Drw	g. Print Fig.		
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TERMINAL		PREPARED FOR ISSUE	Application Examiner				
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